

Amendments to the Abstract:

Please replace the abstract on page 20 with the following amended abstract:

Direct memory access (DMA) control may utilize a ~~direct memory access~~ DMA register adapted to hold a descriptor, and may be used to transfer data from a source to a target. The source and target may be memories, data storage devices, machine-readable media, or one or more devices, apparatuses, systems for holding data. DMA control may communicate with a memory.